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MAIL STOP PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

22857 U.S. PTO
10/796029



New U.S. Patent Application

Title: **SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD
FOR THE SAME**

Inventors and Addresses:

- 1) **Tatsuhiko SHIRAKAWA**
Yokohama-shi, Japan
- 2) **Toshiaki SUGIZAKI**
Yokohama-shi, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application - 46 pages, including 2 independent claims and 20 claims total.
2. Drawings -4 sheets of formal drawings containing 6 figures.
3. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with documents attached.
4. The filing fee is calculated as follows:

Basic Application Filing Fee					\$ 770	\$ 770.00
	Number of Claims		Basic	Extra Claims		
Total Claims	20	-	20	0	x \$18	\$
Independent Claims	2	-	3	0	x \$86	\$
<input type="checkbox"/> Presentation of Multiple Dep. Claim					+\$290	
Subtotal						\$ 770.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 770.00

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5. A check for \$770.00 is enclosed for filing fee.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicants claim the right to priority based on Japanese Patent Application No. 2004-003567, filed on January 9, 2004.

Please address all correspondence with respect to this application to:

**Finnegan, Henderson, Farabow,
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1300 I Street, N.W.
Washington, D.C. 20005-3315**

Please accord this application an application number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: 

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RVB/FPD/sci
Enclosures